

Quadrant IV – Assessment (Module –wise)

Programme	: Bachelor of Science (Second Year)
Subject	: Computer Science
Semester	: IV
Paper Code	: CSC104
Paper Title	: Computer Organization and Operating Systems
Title of the Unit	: Basic Computer Organization and Design
Module Name	: RISC, CISC Architectures
Module Number	: 14
Name of the Presenter:	Mrs. Avani Anil Kharde

Short Answers:

1. What is instruction cycle?
2. With a neat diagram, explain the instruction pipeline processing in RISC architecture
3. Write the advantage of RISC over CISC?
4. Explain about the RISC architecture.
5. Differentiate between RISC and CISC processor.
6. Why is RISC important, and how is it different from CISC?
7. How has RISC design philosophy impacted these technology trends, as well as processor performance?

Multiple Choice Questions :

1. The CISC stands for _____
 - a) Computer Instruction Set Compliment
 - b) Complete Instruction Set Compliment
 - c) Computer Indexed Set Components
 - d) Complex Instruction set computer

2. The computer architecture aimed at reducing the time of execution of instructions is _____
- a) CISC
 - b) RISC
 - c) I SA
 - d) ANNA
3. The RISC processor has a more complicated design than CISC.
- a) True
 - b) False
4. Both the CISC and RISC architectures have been developed to reduce the _____
- a) Cost
 - b) Time delay
 - c) Semantic gap
 - d) All of the mentioned
5. In CISC architecture most of the complex instructions are stored in _____
- a) Register
 - b) Diodes
 - c) CMOS
 - d) Transistors
6. Which of the architecture is power efficient?
- a) CISC
 - b) RISC
 - c) ISA
 - d) IANA
